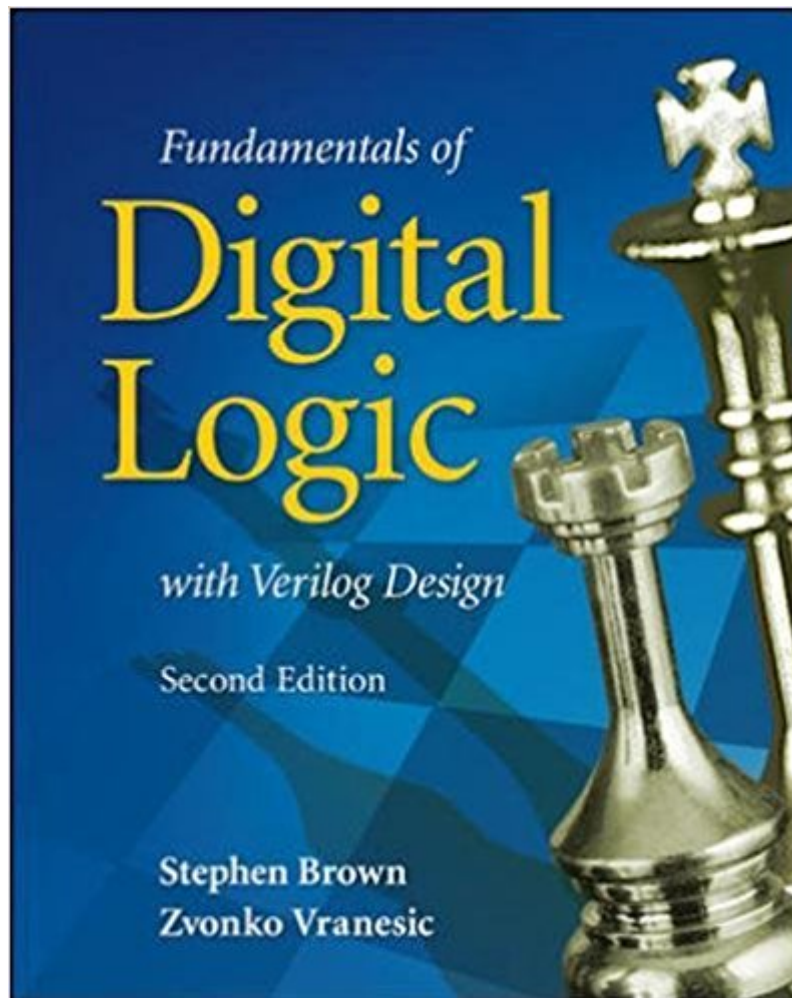




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Fundamentals Of Digital Logic With Verilog Design



Synopsis

Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

Book Information

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Customer Reviews

Zvonko Vranesic (Toronto, Canada) Professor of Computer Engineering at Toronto University,

Canada.

The topics on digital logic and circuits are all fine and explained well enough. Digital circuits at this level are not that complicated. However, the verilog instruction is dated. A lot of the appendices contain tutorials on using verilog in Quartus. These tutorials are for a much older version, and a lot has changed. While it's still possible to figure out, there must be a more up to date version.

very useful to me

As a standalone, the book is pretty hard to learn from. If I couldn't understand concepts from the professor's notes and lectures, the book was of no help to me. It's pretty text heavy, but diagrams are occasionally helpful in laying out what you're looking at. At least until you approach the end of the book. Have not used Verilog yet, so cannot comment on how that works.

While the information provided is extremely valuable, everything is delivered in the least efficient way possible. Diagrams are pretty good, but for a subject matter so heavy on demonstration and concepts, it's text heavy and uninteresting. Definitely requires a good professor as a companion

Bought it for college. Not a bad book - just make sure you buy the version your professor suggest. There are like 3 versions of this book - almost all the same. However, some versions cost a lot more than other ones.

was really good, i am happy i ordered the book from them. logic design book looked like new jkl jkls s jks

This book is poorly written and difficult for those new to digital electronics to understand. I found it particularly difficult because of the jargon used early on. While I realize that it is essential for the student to get used to the rhetoric, it is rather painful to get through the chapters. I read the first few chapters, but found the descriptions so hard to gather that I began relying totally on my in class notes to finish the class out (thankfully earning the A.) My main source of information was Google. This should not have to be the case when a textbook is assigned for a course. However, this book does do a good job of showing examples and how they would work in Verilog. I must say that this is likely the only redeeming quality of this text.

Very prompt. What I expected.

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